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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Complete if Known	
	Application Number	Unknown 10/777957
	Filing Date	Even Date Herewith 02/13/04
	First Named Inventor	Shirley, Paul
	Group Art Unit	Unknown 1746
	Examiner Name	Unknown CA22116
Sheet 1 of 1		Attorney Docket No: 303.774US2

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
BSC ↓ BSC	US-2003/0037400	02/27/2003	Hunter, R W., et al.	15	306.1	12/06/2001
	US-5,860,178	01/19/1999	Nishimura, J, et al.	15	77	03/29/1996
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Examiner Initials *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
BSC	JP-2000-33346	02/02/2000	Disco, KK	B08B	003/02	
BSC	JP-2003-77987	03/14/2003	Sony,	B08B	001/00	
BSC	JP-64-11684	01/17/1989	Takei, Y, et al.			

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
BSC		VAN ZANT, PETER, "Microchip fabrication : a practical guide to semiconductor processing", 4th Edition, New York : McGraw-Hill, Chapter 4 & Chapters 8-9,(2000),87-132 & 193-280	

EXAMINER

[Signature]

DATE CONSIDERED

12/16/04

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached